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Ser. No:		10/634,636	Filing Date:	08/04/2003	Inv(s)	Rinerson et al.				
Title:	e: A 2-Terminal Trapped Charge Memory Device with Voltage Switchable Multi-Level Resistance						ance			
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APPLICATION NO.	FILING DATE	FI	RST NAMED INVE		ATTORNEY DOCKET NO.	CONFURMATION NO.		
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